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November 22, 2006

From: Curtis A. Vock

Matter No.: 408204

To:	Company:	Fax Number:	Phone Number:
Mail Stop: Amendment Commissioner for Patents	U.S. Patent Office	(571) 273-8300	

Number of Pages Transmitted (including this cover sheet): 19

Message:

Applicant(s) Efren M. Lacap et al.

Examiner Jonathan J. Johnson

Serial No. 10/648,586

Group Art No. 1725

Filed 26 August 2003

Confirmation No. 4089

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Applicant(s): Efren M. Lacap, et al.			408204
Serial No.	Filing Date	Examiner	Group Art Unit
10/648,586	August 26, 2003	Jonathan J. Johnson	1725
Invention Wafer-Level Chip Scale Package			
<p>I hereby certify that the following: Facsimile Cover Sheet (1 page); Transmittal Form (1 page); Petition for Extension of Time Under CFR 1.136(a) (1 page, in duplicate); Amendment And Response (14 pages); authorization to charge Deposit Account No. 12-0600 in the amount of \$510.00.00 for the extension of time fee; authorization to charge additional fees that may be required, or credit any overpayment, to Deposit Account No. 12-0600; are being sent via facsimile transmission to Mail Stop: Amendment, c/o the Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 22nd day of November 2006</p>			
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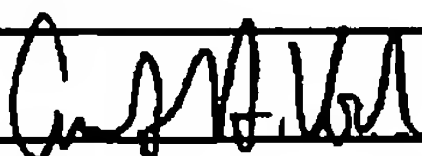
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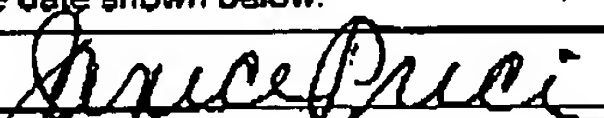
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/648,586
	Filing Date	August 26, 2003
	First Named Inventor	Efren M. Lacap
	Art Unit	1725
	Examiner Name	Jonathan J. Johnson
Total Number of Pages in This Submission		Attorney Docket Number 408204

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Date	November 22, 2006	Reg. No.	38,356

CERTIFICATE OF MAILING 37 CFR 1.10			
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